

ABSTRACT

A fan motor speed control circuit is disclosed. The circuit includes a digital/analog converting unit and a driving unit. The digital/analog converting unit takes charge of receiving a pulse width modulation (PWM) signal and converting it into a voltage signal.

5 The driving unit is connected with the digital/analog converting unit in series for receiving the voltage signal, while the driving unit provides a first predetermined voltage level and a second predetermined voltage level as references. The fan motor is kept to run with a low constant rotation speed when the voltage signal level is higher than the first predetermined voltage level, with a full constant rotation speed when the voltage signal level is lower than
10 the second predetermined voltage level, and with a variable rotation speed when the voltage signal level is lower than the first predetermined voltage level and higher than the second predetermined voltage level.